

12-bit signed output data samples

32-bit phase accumulator "tuning word"

Phase resolution of  $2^{\#}/2^{12}$

Resolution of  $\$s/2^{32} \cdot \$s$  sample frequency

( ) \* d+, signal-to-Noise Ratio !, NR

( ) \* d+, purious frequency - dynamic Range !, \$ - R

iSpedD1o to D1o to CSDN/CO, square and sawtooth output waveforms

2 clock cycles latency

, ample rates of  $1^{**} 2345^1$

- digital oscillators

- digital modulation

- digital up/down conversion

Generation of quadrature complex signals

versatile waveform generation

cl0 in , ample clock rising edge

reset in : synchronous reset low

en in Clock-enable high

~~frequency~~ N is defined as increment as an unsigned 32-bit number controls frequency of

:ll source files are provided as te8t files coded in 93 - I. D e following

